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to VLSI Testing 2 Goal of this Lecture IUnderstand the process oftesting IFamiliar with terms used in testing IView testing as a problem of economics. 3 Introduction toIC Testing IIntroduction ITypes of IC testing IManufacturing tests ITest quality and economy ITest industry. 4

Chapter 1 Introduction to VLSI Testing

GATE ECE's Network Theory, Control Systems, Electronic Devices and Vlsi, Analog Circuits, Digital Circuits, Microprocessors, Signals and Systems, Communications, Electromagnetics, General Aptitude, Engineering Mathematics Previous Years Questions well organized subject wise, chapter wise and year wise with full solutions, provider ExamSIDE.Com

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1) Explain how logical gates are controlled by Boolean logic? In Boolean algebra, the true state is denoted by the number one, referred as logic one or logic high. While, the false state is represented by the number zero, called logic zero or logic low. And in the digital electronic, the logic high is denoted by the presence of a voltage potential.

Top 17 VLSI Interview Questions & Answers

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Write the equivalent decimal value of the given 2's Complement number (101101110011110.1) 2. Design a 4-input NAND gate using only 2-input NAND gates.

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